

Search history

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8673	(sense with amplifier) and (first with input) and (second with input) and (enable with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:05
L2	2525	1 and (first with inverter) and (second with inverter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:05
L3	2434	2 and (first with output) and (second with output)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:05
L4	944	3 and ((first adj inverter) with (first near\$2 input))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:06
L5	841	4 and ((second adj inverter) with (second near\$2 input))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:07
L6	18	5 and (trip with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:10
L7	17	1 and louis.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:07
L8	616	5 and "365"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:08

L9	327	8 and (logic with gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:08
L10	67	9 and ((enable adj signal) with (sense adj amplifier))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:09
L11	1	10 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:09
L12	0	((sense with amplifier) and (first with input) and (second with input) and (first with inverter) and (second with inverter) and (logice with gate) and (first with output) and (second with output) and (trip with signal)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:12
L13	0	((sense with amplifier) and (first with input) and (second with input) and (first with inverter) and (second with inverter) and (logice with gate) and (first with output) and (second with output)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:12
L14	21	((sense with amplifier) and (first adj input) and (second adj input) and (first adj inverter) and (second adj inverter) and (logic with gate) and (first with output) and (second with output)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:12
L15	1	((sense with amplifier) and (first adj input) and (second adj input) and (first adj inverter) and (second adj inverter) and (logic with gate) and (first with output) and (second with output) and (trip with signal)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/21 16:12